

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	14	((("6487246") or ("6240047") or ("6272656") or ("3803594") or ("3646520") or ("20030090307"))). PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/11 15:17
S2	1	clock adj control adj sequential adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 08:48
S3	195	clock adj pulse adj generat\$3 and variation adj (signal or pulse)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 08:52
S4	339	clock adj pulse adj generat\$3 and memor\$3 and master and slave	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 09:02
S5	7	clock adj pulse adj generat\$3 and (clock adj control) and (sequential adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 09:06
S6	297	((low or reduced) adj power) and (sequential adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 13:34
S7	5435	(synchroniz\$5 or sync) (clock adj (pulse or signal)) (master) (slave) memor\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/27 10:22
S8	4697	(synchroniz\$5 or sync) (clock adj (pulse or signal)) (master) (slave) memor\$3 detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 13:42

S9	68	(synchroniz\$5 or sync) (clock adj (pulse or signal)) (master) (slave) memor\$3 detect\$3 (sequential adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 14:43
S10	3249	(365/233).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/19 14:43
S11	2	S10 and (master adj cell) and (slave adj cell)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 14:45
S12	2	S10 (sequential adj circuit) (low adj power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 15:17
S13	5721	(365/230.08,205,189.05).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/19 15:17
S14	339	clock adj pulse adj generat\$3 and memor\$3 and master and slave	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 15:18
S15	2	S13 S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/19 15:18
S16	924	clock and master and slave and (sens\$3 adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/21 10:45

S17	875	clock and master and slave and (sens\$3 adj amplifier) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/21 10:45
S18	448	clock and master and slave and (sens\$3 adj amplifier) and (memory adj array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/21 10:46
S19	1	clock and (master adj (element or cell)) and (slave adj (element or cell)) and (sens\$3 adj amplifier) and (memory adj array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/21 10:48
S20	4	clock and (master adj3 (element or cell)) and (slave adj3 (element or cell)) and (sens\$3 adj amplifier) and (memory adj array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/21 10:48
S22	171	(synchroniz\$5 or sync) (clock adj (pulse or signal)) (master) (slave) memor\$3 (update adj sequen\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/27 13:58
S23	802	(variation adj detection) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/29 15:26
S24	6	(variation adj detection) and (memory adj state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/28 09:57
S25	208	((slave and master) adj (cell or element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/28 09:59

S26	66	((slave and master) adj (cell or element)) and detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/28 10:00
S27	29	((slave and master) adj (cell or element)) and detect\$3 and clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/28 10:00
S28	18	("4677394" "5313501" "5355037" "5513377" "5604775" "5790612" "5828857" "5872959" "5894570" "5910740" "6105144" "6157690" "6177844" "6178123" "6262998" "6282592" "6332010" "6493829").PN.	US-PGPUB; USPAT; USOCR	AND	ON	2005/09/28 12:43
S29	52053	latch and clock and detect\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/09/28 12:43
S30	7512	latch and clock and detect\$3 and low adj power	US-PGPUB; USPAT; USOCR	AND	ON	2005/09/28 12:44
S31	1437	latch and clock and detect\$3 and low adj power and master and slave	US-PGPUB; USPAT; USOCR	AND	ON	2005/09/28 12:44
S32	273	latch and clock and detect\$3 and low adj power and master and slave and memory adj cell	US-PGPUB; USPAT; USOCR	AND	ON	2005/09/28 12:44
S33	267	latch and clock and detect\$3 and low adj power and master and slave and memory adj cell and logic	US-PGPUB; USPAT; USOCR	AND	ON	2005/09/28 12:44
S34	55	latch and clock and detect\$3 and low adj power and master and slave and memory adj cell and logic and "xor"	US-PGPUB; USPAT; USOCR	AND	ON	2005/09/28 13:56
S35	3187	(variation adj detect\$3) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/29 15:26
S36	24	(varia\$4 adj detect\$3) (sequen\$4 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/29 15:30

S37	17	(varia\$4 adj detect\$3) (sequen\$4 adj circuit) ((flip adj flop) or latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/29 15:30
S38	130	(master adj3 memor\$3) (slave adj3 memor\$3) (clock adj3 generator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/30 12:03
S39	715	((master and slave) adj3 (memor\$3 or latch\$2 or (flip-flop or flip adj flop or flipflop))) (clock adj3 generator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/30 12:43
S40	2	(state near2 memor\$3) adj3 control\$4 adj3 clock adj2 (pulse or signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/30 12:14
S41	3	((master and slave) adj3 (memor\$3 or latch\$2 or (flip-flop or flip adj flop or flipflop))) (clock adj3 generator) (variation near2 master)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/30 12:21
S42	6	((master and slave) adj3 (memor\$3 or latch\$2 or (flip-flop or flip adj flop or flipflop))) (clock adj3 generator) (variation adj (signal or pulse))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/09/30 12:44
S43	13	("5557225").URPN.	USPAT	AND	ON	2005/09/30 14:47
S44	8	"6,345,000"	USPAT	AND	ON	2005/09/30 15:45
S45	3691	plurality adj latch\$2	USPAT	AND	ON	2005/09/30 15:45
S46	200	plurality adj latch\$2 (master and slave)	USPAT	AND	ON	2005/09/30 15:45
S47	107	plurality adj latch\$2 (master and slave) (generator)	USPAT	AND	ON	2005/09/30 15:45
S48	77	plurality adj latch\$2 (master and slave) (generator) (detect\$3)	USPAT	AND	ON	2005/09/30 15:48
S49	41	plurality adj latch\$2 (master and slave) (generator) (detect\$3) (flip)	USPAT	AND	ON	2005/09/30 15:46
S50	83	(plural\$3 or multiple) adj latch\$2 (master and slave) (generator) (detect\$3)	USPAT	AND	ON	2005/09/30 15:55

S51	442	(matsushita or kotobuki or panasonic).as. ("365").clas.	USPAT	AND	ON	2005/09/30 15:56
S52	3	(matsushita or kotobuki or panasonic).as. ("365").clas. (master) (slave)	USPAT	AND	ON	2005/09/30 15:56
S53	151	(matsushita or kotobuki or panasonic).as. ("365").clas. (clock)	USPAT	AND	ON	2005/09/30 15:57
S54	74	(matsushita or kotobuki or panasonic).as. ("365").clas. (clock) (detect\$3)	USPAT	AND	ON	2005/09/30 16:00
S55	0	((master and slave) adj (cell or memory) adj group) (clock adj domain)	USPAT	AND	ON	2005/09/30 16:01
S56	0	((master) adj (cell or memory) adj group) (clock adj domain)	USPAT	AND	ON	2005/09/30 16:01
S57	5	((master and slave) adj (cell or memory)) (clock adj domain)	USPAT	AND	ON	2005/09/30 16:01
S58	718	(327/199).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 12:37
S59	94	S58 generator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 12:37
S60	38	S59 clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:08
S61	14	S60 master	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 12:38
S62	1809	(master and slave) adj (latch or (flip adj flop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:12

S63	25	S62 S58	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:46
S64	5738	(365/230.08,205,189.05).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:12
S65	80	S62 S64	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:12
S66	3263	(365/233).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 13:12
S67	91	S62 S66	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:12
S68	12	S62 (clock near2 stop) (low adj power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 14:36
S69	886	(latch or (flip adj flop)) ((pulse or clock) adj generator) feedback compari\$3 sens\$3 (low adj power) memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/13 09:36
S70	4	(latch or (flip adj flop)) ((pulse or clock) adj generator) feedback compari\$3 sens\$3 (low adj power) memory (master adj cell)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 14:45

S71	32	(latch or (flip adj flop)) ((pulse or clock) adj generator) feedback compari\$3 sens\$3 (low adj power) memory (sequential adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 14:48
S72	20	SASAGAWA-YUKIHIRO.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/11 15:17
S73	19	("3610964" "4893028" "5038059" "5059818" "5081370" "5321368").PN. OR ("5557225"). URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2005/10/13 13:22
S74	62	(sequential adj circuit) ((master and slave) adj (cell or latch or register or memory))	US-PGPUB; USPAT; USOCR	AND	ON	2005/10/13 13:23
S75	25	(sequential adj circuit) ((master and slave) adj (cell or latch or register or memory)) detect\$3 generat\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/10/13 13:23
S76	14	(sequential adj circuit) ((master and slave) adj (cell or latch or register or memory)) detect\$3 (signal or pulse) adj generat\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/10/13 13:50
S77	1	(request adj signal) (request adj update adj signal) (clock or signal or pulse) adj generat\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/10/13 13:52
S78	2	(request adj signal) (request adj update adj signal) (clock or signal or pulse)	US-PGPUB; USPAT; USOCR	AND	ON	2005/10/13 13:51
S79	107	(request adj signal) (request near update) adj signal (clock or signal or pulse)	US-PGPUB; USPAT; USOCR	AND	ON	2005/10/13 13:52
S80	38	(request adj signal) (request near update) adj signal (clock or signal or pulse) adj generat\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/10/13 13:52
S81	14	((("6487246") or ("6240047") or ("6272656") or ("3803594") or ("3646520") or ("20030090307"))). PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/23 10:11
S82	1	clock adj control adj sequential adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11

S83	196	clock adj pulse adj generat\$3 and variation adj (signal or pulse)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S84	348	clock adj pulse adj generat\$3 and memor\$3 and master and slave	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S85	7	clock adj pulse adj generat\$3 and (clock adj control) and (sequential adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S86	309	((low or reduced) adj power) and (sequential adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S87	5556	(synchroniz\$5 or sync) (clock adj (pulse or signal)) (master) (slave) memor\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S88	4807	(synchroniz\$5 or sync) (clock adj (pulse or signal)) (master) (slave) memor\$3 detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S89	70	(synchroniz\$5 or sync) (clock adj (pulse or signal)) (master) (slave) memor\$3 detect\$3 (sequential adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S90	3309	(365/233).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/23 10:11

S91	2	S90 and (master adj cell) and (slave adj cell)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S92	2	S90 (sequential adj circuit) (low adj power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S93	5800	(365/230.08,205,189.05).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/23 10:11
S94	348	clock adj pulse adj generat\$3 and memor\$3 and master and slave	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S95	2	S93 S94	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S96	943	clock and master and slave and (sens\$3 adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S97	894	clock and master and slave and (sens\$3 adj amplifier) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S98	463	clock and master and slave and (sens\$3 adj amplifier) and (memory adj array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11

S99	1	clock and (master adj (element or cell)) and (slave adj (element or cell)) and (sens\$3 adj amplifier) and (memory adj array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S10 0	4	clock and (master adj3 (element or cell)) and (slave adj3 (element or cell)) and (sens\$3 adj amplifier) and (memory adj array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S10 1	178	(synchroniz\$5 or sync) (clock adj (pulse or signal)) (master) (slave) memor\$3 (update adj sequen\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S10 2	821	(variation adj detection) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S10 3	6	(variation adj detection) and (memory adj state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S10 4	209	((slave and master) adj (cell or element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S10 5	66	((slave and master) adj (cell or element)) and detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S10 6	29	((slave and master) adj (cell or element)) and detect\$3 and clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11

S10 7	18	("4677394" "5313501" "5355037" "5513377" "5604775" "5790612" "5828857" "5872959" "5894570" "5910740" "6105144" "6157690" "6177844" "6178123" "6262998" "6282592" "6332010" "6493829").PN.	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S10 8	52659	latch and clock and detect\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S10 9	7667	latch and clock and detect\$3 and low adj power	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S11 0	1476	latch and clock and detect\$3 and low adj power and master and slave	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S11 1	283	latch and clock and detect\$3 and low adj power and master and slave and memory adj cell	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S11 2	277	latch and clock and detect\$3 and low adj power and master and slave and memory adj cell and logic	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S11 3	58	latch and clock and detect\$3 and low adj power and master and slave and memory adj cell and logic and "xor"	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S11 4	3256	(variation adj detect\$3) and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S11 5	25	(varia\$4 adj detect\$3) (sequen\$4 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S11 6	18	(varia\$4 adj detect\$3) (sequen\$4 adj circuit) ((flip adj flop) or latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S11 7	131	(master adj3 memor\$3) (slave adj3 memor\$3) (clock adj3 generator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11

S11 8	719	((master and slave) adj3 (memor\$3 or latch\$2 or (flip-flop or flip adj flop or flipflop))) (clock adj3 generator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S11 9	2	(state near2 memor\$3) adj3 control\$4 adj3 clock adj2 (pulse or signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S12 0	3	((master and slave) adj3 (memor\$3 or latch\$2 or (flip-flop or flip adj flop or flipflop))) (clock adj3 generator) (variation near2 master)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S12 1	6	((master and slave) adj3 (memor\$3 or latch\$2 or (flip-flop or flip adj flop or flipflop))) (clock adj3 generator) (variation adj (signal or pulse))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S12 2	13	("5557225").URPN.	USPAT	AND	ON	2005/11/23 10:11
S12 3	8	"6,345,000"	USPAT	AND	ON	2005/11/23 10:11
S12 4	3717	plurality adj latch\$2	USPAT	AND	ON	2005/11/23 10:11
S12 5	202	plurality adj latch\$2 (master and slave)	USPAT	AND	ON	2005/11/23 10:11
S12 6	107	plurality adj latch\$2 (master and slave) (generator)	USPAT	AND	ON	2005/11/23 10:11
S12 7	41	plurality adj latch\$2 (master and slave) (generator) (detect\$3) (flip)	USPAT	AND	ON	2005/11/23 10:11
S12 8	77	plurality adj latch\$2 (master and slave) (generator) (detect\$3)	USPAT	AND	ON	2005/11/23 10:11
S12 9	83	(plural\$3 or multiple) adj latch\$2 (master and slave) (generator) (detect\$3)	USPAT	AND	ON	2005/11/23 10:11
S13 0	448	(matsushita or kotobuki or panasonic).as. ("365").clas.	USPAT	AND	ON	2005/11/23 10:11
S13 1	3	(matsushita or kotobuki or panasonic).as. ("365").clas. (master) (slave)	USPAT	AND	ON	2005/11/23 10:11
S13 2	152	(matsushita or kotobuki or panasonic).as. ("365").clas. (clock)	USPAT	AND	ON	2005/11/23 10:11

S13 3	74	(matsushita or kotobuki or panasonic).as. ("365").clas. (clock) (detect\$3)	USPAT	AND	ON	2005/11/23 10:11
S13 4	0	((master and slave) adj (cell or memory) adj group) (clock adj domain)	USPAT	AND	ON	2005/11/23 10:11
S13 5	0	((master) adj (cell or memory) adj group) (clock adj domain)	USPAT	AND	ON	2005/11/23 10:11
S13 6	5	((master and slave) adj (cell or memory)) (clock adj domain)	USPAT	AND	ON	2005/11/23 10:11
S13 7	721	(327/199).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/23 10:11
S13 8	94	S137 generator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S13 9	38	S138 clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S14 0	14	S139 master	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S14 1	1827	(master and slave) adj (latch or (flip adj flop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S14 2	5800	(365/230.08,205,189.05).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/23 10:11
S14 3	80	S141 S142	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11

S14 4	3309	(365/233).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/23 10:11
S14 5	91	S141 S144	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S14 6	25	S141 S137	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S14 7	14	S141 (clock near2 stop) (low adj power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S14 8	4	(latch or (flip adj flop)) ((pulse or clock) adj generator) feedback compari\$3 sens\$3 (low adj power) memory (master adj cell)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S14 9	33	(latch or (flip adj flop)) ((pulse or clock) adj generator) feedback compari\$3 sens\$3 (low adj power) memory (sequential adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S15 0	905	(latch or (flip adj flop)) ((pulse or clock) adj generator) feedback compari\$3 sens\$3 (low adj power) memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S15 1	20	SASAGAWA-YUKIHIRO.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:11
S15 2	19	("3610964" "4893028" "5038059" "5059818" "5081370" "5321368").PN. OR ("5557225").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11

S15 3	62	(sequential adj circuit) ((master and slave) adj (cell or latch or register or memory))	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S15 4	25	(sequential adj circuit) ((master and slave) adj (cell or latch or register or memory)) detect\$3 generat\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S15 5	14	(sequential adj circuit) ((master and slave) adj (cell or latch or register or memory)) detect\$3 (signal or pulse) adj generat\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S15 6	1	(request adj signal) (request adj update adj signal) (clock or signal or pulse) adj generat\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S15 7	2	(request adj signal) (request adj update adj signal) (clock or signal or pulse)	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S15 8	108	(request adj signal) (request near update) adj signal (clock or signal or pulse)	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S15 9	39	(request adj signal) (request near update) adj signal (clock or signal or pulse) adj generat\$3	US-PGPUB; USPAT; USOCR	AND	ON	2005/11/23 10:11
S16 0	29	(master slave) adj (cell or latch or flipflop or (flip adj flop)) (variation or "xnor" or "xor") (sequential adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/11/23 10:28